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INFORMATION DISCLOSURE STATEMENT BY APPLICANT			
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Complete if Known			
Application Number		10/750,582	
Filing Date		December 29, 2003	
First Named Inventor		Tsai, Vicki	
Art Unit		2128	
Examiner Name		Ferris III, Fred O	
Sheet	1	of	2
Attorney Docket No: 80107.095US1			

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Examiner Initials *	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
z		<u>International Search Report and Written Opinion of the International Searching Authority: Dated 12/06/2005: PCT/US2005/010387, 16 pgs.</u>	
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Substitute Disclosure Statement Form (PTO-1449)

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24		ZHANG, et al., "Interconnect Architecture Exploration for Low-Energy Reconfigurable Single-Chip DSPs", <u>VLSI, 99, PROCEEDINGS, IEEE COMPUTER SOCIETY WORKSHOP ON ORLANDO, FL, (April, 08, 1999), 2-8</u>	

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